

Silicon for the Wireless Networks of Tomorrow

This is the second in a series of interviews with Top Technologists at Freescale explaining their technology innovations. In this video Processor Core & Platform Development Engineer, Dac Pham, explains how he and his team created and designed QorIQ Qonverge processors for the wireless networks for tomorrow, which combines multiple Power Architecture cores and high-performance StarCore DSPs.

www.freescale.com [1]

Posted by Janine E. Mooney, Associate Editor

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