

CMOS transmitter ICs achieve line rates to 31.8 Gbps/channel for 100G networks



Applied Micro Circuits announced what it asserts is the world's fastest CMOS transmitter ICs for Dense Wavelength Division Multiplexing line-side applications, which support power-efficient, cost-effective deployments in 100-Gigabit per second optical metro, regional and long-haul networks. With line rates up to 31.8 Gbps per channel, the S28032 DQPSK Mux chips are positioned to pave the way for wide-scale deployment in 100G DWDM Optical Transport Networks (OTN) as operators increasingly seek higher bandwidth solutions and greater network density. CMOS ICs provide a scalable high-volume, low-power solution for ramping 100G deployments. The IC provides a flexible interface to connect either FPGAs or ASICs running hard or soft-decision FEC (Forward-Error Correction) on a 10 or 20-lane SFI-S bus to optical drivers for PM-QPSK and other DWDM optical systems. The S28032 supports line rates from 2.7 Gbps through 31.8 Gbps for applications with 7, 15, or 20 percent FEC overhead. The company reports early access partners, who represent significant market share, are passing traffic error-free in the lab in preparation for field trials.

Applied Micro Circuits

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