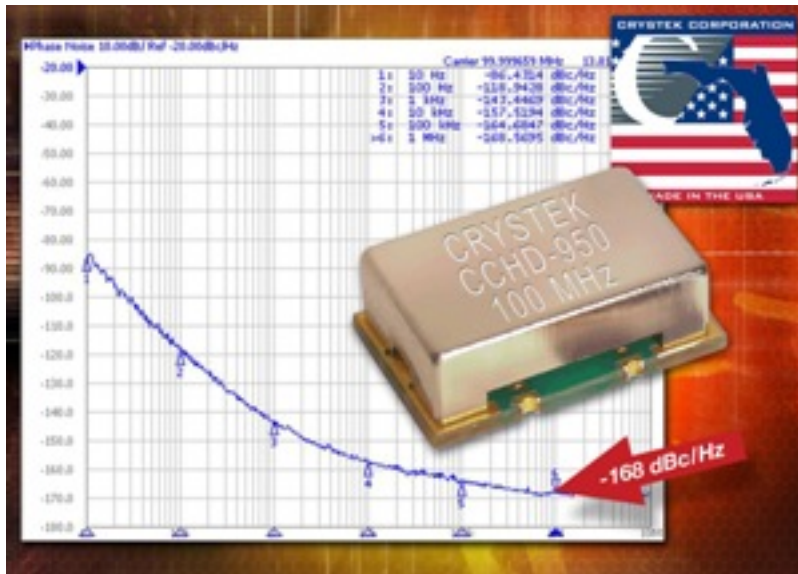


Clock oscillator offers ultra-low phase noise

Published on Electronic Component News (<http://www.ecnmag.com>)

Clock oscillator offers ultra-low phase noise



Crystek Corporation's ultra-low phase noise CCHD-950 Series HCMOS Clock Oscillator pushes the phase noise performance benchmark with a -168 dBc/Hz noise floor (100 MHz model). The CCHD-950 generates frequencies between 45 MHz and 130 MHz, with 50 MHz, 80 MHz, 100 MHz and 130 MHz offered as standard. A high-Q crystal and 3rd overtone technology provide the ultra-low phase noise and low-jitter performance, making the CCHD-950 very useful in synthetic instrumentation such as VXI/PXI. Generating no sub-harmonics, the CCHD-950 requires an input supply voltage of 3.3 Vdc consuming 15 mA of current. This high performance oscillator is available in an FR5 9x14 mm SMD package. An extended temperature operating range of -40°C to +85°C is also available. Applications include high definition TV, avionics, low phase signal sources, and test & measurement.

Crystek Corporation

800-237-3061

239-561-3311

sales@crystek.com [1]

www.crystek.com [2]

Source URL (retrieved on 02/01/2015 - 8:53am):

<http://www.ecnmag.com/products/2012/05/clock-oscillator-offers-ultra-low-phase-noise>

Links:

[1] <mailto:sales@crystek.com>

[2] <http://www.crystek.com/>