VIA Unveils Nano E-Series at ESC 2010

VIA Technologies, Inc, a leading innovator of power efficient x86 processor platforms, today announced the VIA Nano E-Series processor, bringing native 64-bit software support, virtualization capabilities and extended longevity support to embedded markets.

The move to 64-bit software architecture is an essential transition for the future of the embedded industry. Forthcoming operating systems such as Windows® Embedded Standard 7 will be able to leverage a 64-bit software eco system that provides up to double the amount of data a CPU can process per clock cycle. This translates in to greater ease in manipulating large data sets and an overall performance boost compared to non-native x64 architectures.

"VIA Nano E-Series processors have been designed to facilitate the shift towards new technologies that will shape the embedded industry for years to come," said Daniel Wu, Vice President, VIA Embedded Platform Division, VIA Technologies, Inc. "The VIA Nano E-Series will mean that technologies such as 64-bit computing and virtual software deployment will become the norm, not the exception for tomorrow's embedded system developers."

The VIA Nano E-Series also supports VIA VT virtualization technology, allowing legacy software and applications to be used in virtual scenarios without impacting on performance. And as with all VIA processors, the VIA PadLock™ Security Engine offers hardware based data encryption standards that meet widely accepted international standards.

Visitors to the VIA Technologies booth at ESC Silicon Valley 2010 will be able to see the VIA Nano E-Series in action. Join VIA at Embedded System Conference 2010, April 27 - 29, San Jose, McEnery Convention Center, Booth No.1238.

For more details, please visit:

http://www.via.com.tw/en/company/events/esc2010/index.jsp [1]

VIA Nano E-Series Highlights

Available at speeds from 800MHz to 1.8GHz, VIA Nano E-Series processors have guaranteed product longevity of seven years and are built on the successful 64-bit, superscalar architecture that powers the VIA Nano 3000 Series and 2000 Series processors.

All VIA Nano E-Series processors are fully compatible with a range of fully integrated media system processors including the VIA VX800, VIA VX855 and forthcoming VIA VX900 and VIA VN1000 digital media chipsets, offering exceptional hardware acceleration of the most demanding HD video codecs and industry leading 3D graphics capabilities.

- World's most power-efficient out-of-order x86 architecture
- Full support for 64-bit operating systems
- High-performance superscalar processing
- Most efficient speculative floating point algorithm
- Full CPU virtualization support
- Advanced power and thermal management
- Leading-edge hardware security features
- Pin-to-pin compatibility with VIA Nano, VIA C7 and VIA Eden Processors
- DDR2 and DDR3 memory support

VIA Nano E-Series Availability

VIA Nano E-Series processor samples are available now to project customers and will be available in the following product skus:

Product Name	Speed	VIA V4 Bus	Idle Power
L3050	1.8GHz	800MHz	500mW
U3100	1.3+GHz	800MHz	100mW
U3300	1.2GHz	800MHz	100mW
U3500	1.0GHz	800MHz	100mW
U3400	800MHz	800MHz	100mW

For further information about the VIA Nano processor family and the VIA Nano E-Series, please go to:
http://www.via.com.tw/en/products/processors/nano/index.jsp#e_series [2]

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