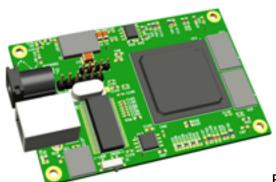
SDRAM Module Stores 64 MB



Based on the 4,000,000 gate Xilinx Spartan-3 FPGA, Opal Kelly's XEM3050 FPGA USB 2.0 module presents 64 MB SDRAM using 2 independent 32 MB SDRAMs. The component offers 8 Mb SPI Serial Flash memory for non-volatile storage; a separate Xilinx Platform Flash for FPGA configuration data; 512k x18 synchronous SRAM; and 2 switching power supplies. API is available for Microsoft Windows XP/Vista, Mac OS X, and Linux FedoraCore 7 with languages including C, C++, C#, Ruby, Python, and Java. Integrated FrontPanel software handles the interaction between the software and the FPGA design, and DLL support is provided for third-party tools including Matlab and LabVIEW. Applications include image capture and signal processing, data acquisition, and digital communication. Pricing is \$749.95 in 1 to 9 quantities, \$699.95 for 10 to 49 quantities, and \$649.95 for 50 quantities and up.

Opal Kelly 217-391-3724, www.opalkelly.com [1]

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