

## **Processor core designed for automotive safety-compliant applications**

Synopsys' [DesignWare ARC EM SEP](#) [1] (Safety Enhancement Package) Processor core for ISO 26262 automotive safety-compliant applications is a highly efficient 32-bit core that delivers performance up to 300 MHz and power consumption as low as 16  $\mu$ W/MHz on typical 65LP silicon process, with integrated hardware safety features that enable ASIL D compliance in support of the ISO 26262 standard. In addition, the ASIL D ready certified DesignWare ARC MetaWare Compiler helps software developers accelerate the development of ISO 26262-compliant code. The combination of a safety-enhanced processor and compiler makes the ARC EM SEP core ideally suited for system-on-chips (SoCs) designed for embedded automotive applications such as movement and acceleration sensors, advanced driver assistance systems and electric power steering.

### Highlights:

- Optimized for high-efficiency, low-power embedded automotive applications
- Automotive Safety Integrity Level D (ASIL D) ready DesignWare ARC MetaWare Compiler enables development of ISO 26262-compliant software
- Integrated safety features include parity support and error-correcting code (ECC) technology
- Detailed IP safety documentation eases certification of ARC EM SEP-based systems

[www.synopsys.com](http://www.synopsys.com) [2]

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### **Links:**

[1] [http://app.connect.synopsys.com/e/er?elq\\_mid=5064&elq\\_cid=273481&am p;s=700&lid=4239&elq=883f838ccb974c5a90932362c4aaf19b](http://app.connect.synopsys.com/e/er?elq_mid=5064&elq_cid=273481&am p;s=700&lid=4239&elq=883f838ccb974c5a90932362c4aaf19b)

[2] <http://www.synopsys.com>