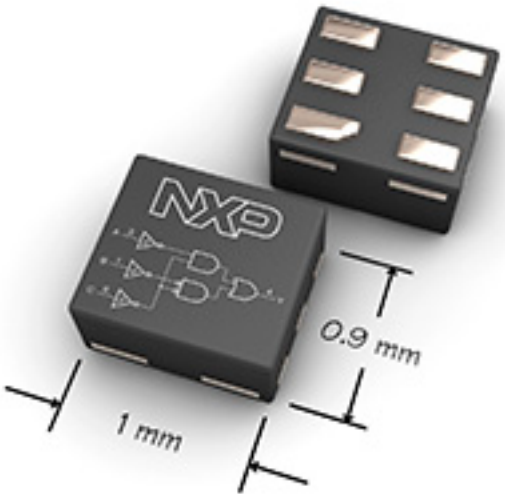
Logic family asserts to be fastest family of devices with the lowest power consumption



NXP Semiconductors N.V. introduced the AXP logic family designed for high-performance, low-voltage and low-power applications. The new family is fully specified at the 0.8-voltV supply range, making it the industry's fastest family of devices with the lowest power consumption and highest output drive, according to the company. Currently available in the smallest MicroPak (leadless) packages, designers now have a balance of power, speed and packaging in standard logic. Configurable logic devices in the AXP family can perform different logic functions, thereby reducing bill-of-material part count, cost and qualification effort, as well as enabling a smaller package footprint, which is vital in portable devices. Ideally suited for leading-edge portable devices such as smartphones, tablets, digital cameras, and portable medical devices where longer battery life is critical, the AXP's low-power and high-performance logic family is asserted to minimize system power dissipation and conserve battery life. Features tpd of 2.9 ns at Vcc of 1.8 V, wide supply voltage range (0.7 V to 2.75 V), fully specified at 0.8 V, Schmitt-trigger action on all inputs, 4.5 mA balanced output drive, and over-voltage tolerant I/Os.

NXP Semiconductors N.V., www.nxp.com [1]

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