Silicon photonics chips boast 100 gigabits per second 4x25 QSFP package

Kotura [1] announced a silicon photonics industry first. The company unveiled its Optical Engine [2] in a Quad Small Form-factor Pluggable (QSFP) package. Kotura's Optical Engine uses Wavelength Division Multiplexing (WDM), in which different signals can share the same path. Kotura is the only silicon photonics provider to offer WDM and now chalks up another industry first as the only silicon photonics provider to demonstrate WDM in a 100 gigabits per second (Gb/s) 4x25 QSFP package with 3.5 watts of power.

Kotura's Optical Engine provides an inexpensive, small form factor that reduces power consumption and provides a high level of integration. Consuming only 3.5 watts of power, Kotura is addressing the need for green solutions for 100G pipes desired by data centers [3] and high performance computers [4] (HPC).

The QSFP package has become the industry standard footprint for 4x10G and 40G Ethernet in data centers as well as 40G and 56G Infiniband in HPC. Kotura predicts that the same package will become the industry's volume standard for 100G networks in both data centers and HPC applications.

Kotura's WDM can scale from four channels to many more, on the same chip. At 100G and higher, Kotura's customers need WDM to avoid the use of expensive ribbon fiber, parallel connectors and patch panels. For large data centers, reaches of 30 meters to 2 kilometers are common and expensive ribbon fiber dominates the

interconnect fabric costs. For Active Optical Cables and very short-reach links, Kotura also offers a parallel version of its 100G Optical Engine.

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http://www.ecnmag.com/product-releases/2013/03/silicon-photonics-chips-boast-100-gigabits-second-4x25-qsfp-package?qt-recent_content=0

Links:
[1] http://kotura.com/
[2] http://www.marketwire.com/press-release/Silicon-Photonics-Innovator-Kotura-Unveils-Low-Power-100-Gb-s-Optical-Engine-1626790.htm
[3] http://www.kotura.com/products-apps/data-center.html
[4] http://www.kotura.com/products-apps/high-performance-computing.html