Full electrical and conformance test support now available for test fixtures

Tektronix, Inc., announced that it is adding full electrical verification and conformance test support for JEDEC DDR4, DDR3L and LPDDR3 standards, giving design engineers the tools they need to bring chips and systems that incorporate these next generation memory technologies to market.

As memory standards published by JEDEC continue to evolve to higher speeds and reduced power consumption, debug and validation challenges increase as well. For instance, the faster read/write access and lower operating voltages of the new standards require improved signal fidelity from test fixtures. DDR4 introduces a number of new, more complex tests including variable VRef, Eye Masks and Jitter. With the only oscilloscope-based JEDEC conformance solution in the industry that supports all variants of DDR, Tektronix speeds up engineering PHY verification of JEDEC standards testing.

To support memory interface electrical conformance testing and debug of the new standards, Tektronix now offers integrated support for the next generation memory standards in its popular DDRA analysis software. Available for MSO/DPO/DSA70000, DPO7000 and MSO/DPO5000 Series oscilloscopes, it now supports all generations of DDR standards, from the first generation through to the cutting edge. By automating test setup and execution, DDRA gives memory designers the confidence that they are in full conformance with memory standards. Should a memory system fail conformance tests, designers can quickly switch to debug mode and use tools like the powerful Visual Trigger capability on Tektronix oscilloscopes to isolate events of interest for deeper root-cause analysis with the DPOJET Jitter and Eye measurement toolkit.

Tektronix now offers the widest array of probing solutions covering all of the memory technologies including new interposers (DIMM, PoP and embedded) available for DDR4 and LPDDR3. The controlled impedance and trace length matching on these new interposers in combination with de-embedding filters for the oscilloscope ensure an accurate representation of the signal.

In addition to these important updates, Tektronix offers in-depth test support for memory including high channel count memory system validation with the TLA7000 Logic Analyzer, along with capture of both logic and analog signals with the MSO70000 Series. The Tektronix MSO70000, the industry's highest performing mixed signal oscilloscope, is ideal for integration of command-address bus logic capture on digital channels for precise read/write burst detection.

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