Crystal clock oscillator includes phase noise performance of -80dBc/Hz at 10Hz offset

IQD's new CFPS-115 crystal clock oscillator offers two key features; low phase noise and low current consumption. At 40.0MHz the phase noise performance is -80dBc/Hz at 10Hz offset and -145dBc/Hz at 1kHz offset, figures which are around 5dBc better than similar design parts. In addition, the new model offers low power consumption of around only 7mA; this compares with a normal SMD oscillator where current consumption would typically be around 20mA at 32.0MHz.

The device is available at any frequency between 26.0MHz and 44.0MHz and offers a standard 45/55% duty cycle HCMOS output. Operating from a standard 3.3V source the device also has tri-state capability which enables the unit to be isolated in circuit for operations such as in-circuit fault finding. This new model is housed in a 2.5 x 2.0 x 0.8mm hermetically sealed ceramic package incorporating a metal lid, the use of which aids in the reduction of EMI radiation. Operating over a wide temperature range of –40 to +85°C the device can be specified down to a stability of ±30ppm.

The CFPS-115 is a very versatile oscillator designed to be used in a myriad of applications such as computing, aviation, data logging and monitoring equipment where low phase noise and low power are critical to the end product performance.

Further details will be available at the IQD stand 314 in Hall B5 at Electronica 2012.

www.iqdfrequencyproducts.com [1].

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http://www.ecnmag.com/product-releases/2012/11/crystal-clock-oscillator-includes-phase-noise-performance-80dbc/hz-10hz-offset?qt-recent_content=0

Links:
[1] http://www.iqdfrequencyproducts.com