Corelis releases new CD Version 7.7 Boundary-Scan Tool Suite

Corelis [1] announced today the availability of version 7.7 of its ScanExpress [2] Boundary-Scan Tool Suite. This new version covers a wide range of new features and enhancements including improved constraints handling, support for multi-core devices, and new JTAG Embedded Test [3] support for additional Freescale [4] and Texas Instruments [5] processors.

Software improvements include:

- Separate global and local constraint entry within the ScanExpress TPG [6] preparation GUI.
- Inverted transparency model support along with a new constraint to identify inverted nets.
- Ability to associate multiple BSDL files to a single JTAG device.
- FlexNet network licensing for ScanExpress JET [3].
- NAND Flash bad block management support in ScanExpress JET [3].
- JET [3] support for Freescale i.MX35 [4] and Texas Instruments AM18x [5] processors.
- MSP430F5xxx programming support.
- Optimization of STAPL file execution for improved throughput.

Current Corelis customers that have a valid maintenance contract can now access the new version 7.7 CD through the Corelis support website [7]. To inquire about a maintenance package or to obtain a full list of improvements, please contact the Corelis sales team at (562) 926-6727; or send an email to sales@corelis.com [8].

As always, Corelis offers free training to all clients so that they may immediately utilize the various new product features and innovations now available. Class schedules and registration information can be found on the Corelis website at:

http://www.corelis.com/education/JTAG_Boundary-Scan_Seminars_and_Training.htm [9]

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Links:
[1] http://www.corelis.com/
[2] http://www.corelis.com/products-JTAG/JTAG_Products_Software_index.htm
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