XJTAG releases v2.6 boundary scan software

CAMBRIDGE, England, 30th November 2011 - XJTAG today launches version 2.6 of its industry-leading boundary scan software.

The XJTAG Development System now comes with a new library that can auto-suggest the correct file based on the Bill of Materials (BOM) and net list information, enabling even faster set-up times.

The new library, which is pre-installed with thousands of device files, is simple to use. Once the BOM information has been entered, the user will be prompted to hit the 'Suggest' button. This brings up a selection of possible files, including common logic devices and a broad range of other parts.

"At XJTAG our aim is to abstract people away from the complexity of the JTAG IEEE

1149.x standard, making it easier to use," said Simon Payne, CEO of XJTAG. "World leading companies are already benefiting from the advantages that using XJTAG provides. The release of XJTAG v2.6, with its automated matching capability, is a significant step in helping engineers achieve even faster board set up times. This in turn will help to improve time to market, which is crucial to anyone competing in today's global economy."

XJTAG now also comes with a new .NET interface and LabVIEW VIs to make it easier to integrate with third party test solutions. These new interfaces also make it possible to use formatted XJEase text output and to access the Layout Viewer.

The new XJTAG Development System is now available on a 30-day free trial. If you would like to see how XJTAG can significantly reduce test-development and debugging time on your own electronics system, visit: www.xjtag.com [1].

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[1] http://www.xjtag.com