World’s First Serial RapidIO Gen2 Protocol Analyzer

Nexus Technology, Inc. has today introduced Serial RapidIO Gen2 analysis tools for next generation serial buses. This product will be used in conjunction with a Tektronix logic analyzer to enable non-intrusive monitoring and analysis of target systems from the physical coding sublayer to full-duplex link protocol, as well as a generator for stimulus injection at all speeds of Serial RapidIO up to 6.25Gbps.

The NEX-SRIO2 receiver enables acquisition of x4/x2/x1, full-duplex mode links using a Tektronix Logic Analyzer. The Serial RapidIO data is automatically descrambled, decoded and aligned for easy debug and analysis. Real-time error checking capabilities provided by NEX-SRIO2 allow the user to quickly root-cause system level failures.

Real time filtering capability can be used to filter out IDLE sequences and other type of packets to more efficiently capture and store data. This allows the user to capture an event of interest as well as the conditions that resulted in the occurrence of that event on the Serial RapidIO bus.

The NEX-SRIO2 transmitters generate SRIO packets, control symbols and 8b/10b characters in x4/x2/x1 mode. This product also supports generation of proper idle sequences, injection of disparity errors and lane skew. It can also optionally loop on a predefined pattern up to 64k Symbols.

These transmitters allow the testing of Serial RapidIO receivers on the users system for different link conditions and error recovery on occurrence of different types of link errors. it can also be used as a substitute for the Serial RapidIO transmitter on

the users system to quickly recreate infrequent events and test the system response.

"Nexus Technology’s second generation of Serial RapidIO analyzers provides fast and smart analysis capabilities for debug and validation engineers. When coupled with the Tektronix logic analyzer’s deep memory, powerful triggering, and cross-bus correlation capabilities, our customers have the best tools in the industry for complete system debug and validation," said Rob Shelsky, President, Nexus Technology, Inc.

"Tektronix is pleased that our relationship with Nexus Technology continues to bring to market solutions that meet the debug and validation needs of engineers working with advanced serial technologies. The Serial RapidIO (SRIO) Gen2 Protocol Analyzer coupled with a Tektronix Logic Analyzer provides a complete debug solution for next generation Serial RapidIO designs," said Mike Rizzo, Director of Marketing, Tektronix Logic Analyzer Product Line.

More information can be found online at www.nexustechnology.com [1].

www.tektronix.com [2].

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[1] http://www.nexustechnology.com
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