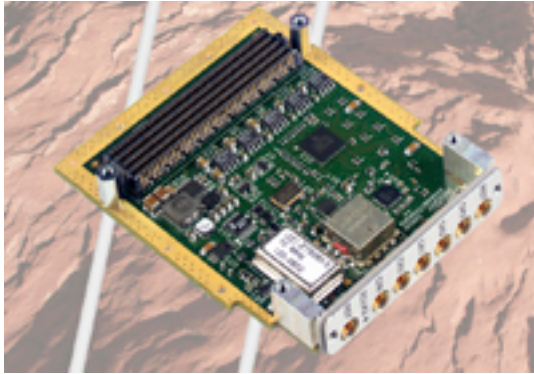


# Quad Channel Clock Generator FMC Card Features Low Jitter



Curtiss-Wright Controls Embedded Computing has introduced an FPGA Mezzanine Card (FMC/VITA 57) module, the FMC-XCLK2. The quad channel clock generator card features low jitter and phase matched outputs. It is available in both air-cooled and conduction-cooled rugged versions, and is said to provide the high quality clock source required for high frequency data sampling applications. It can support RF output frequencies ranging from 50 MHz to >2 GHz. This card is positioned as an alternative source for clocking and synchronizing I/O, whether provided by the company's ADC and DAC FMC cards or the customer's own I/O. The card is designed for use in demanding military applications such as electronic counter measures (ECM), software defined radio (SDR), and RADAR that require high-speed ADC and DAC components. The FMC-XCLK2's RF clocks are derived from either an onboard or external 10 MHz master reference source. The RF output frequency is defined by a build option using a narrowband, but low jitter, VCO/PLL frequency multiplier. An FMC-XCLK2 can also be used as a 10MHz reference clock source to other system components such as clock generators.

### Curtiss-Wright Controls Embedded Computing

703-779-7800, [www.cwcembedded.com](http://www.cwcembedded.com) [1]

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[1] <http://www.cwcembedded.com/>