## Software Interface Standard Targets Cortexbased MCUs



Enabling consistent software interfaces to the

processor for silicon vendors and middleware providers, ARM's ARM Cortex Microcontroller Software Interface Standard (CMSIS) vendor-independent hardware abstraction layer is designed for the Cortex-M processor series. The offering allows silicon vendors to focus their resources on the differentiating peripheral functions of their products, and eliminates the need to maintain their own individual and incompatible standards for programming a MCU. The standard has been designed to be fully scalable to ensure that it is suitable for all Cortex-M processor series MCUs from the smallest 8-KB device to devices with communication peripherals, such as Ethernet or USB-OTG. The CMSIS has been developed in close partnership with Atmel, IAR, KEIL, Luminary Micro, Micrium, NXP, SEGGER, and STMicroelectronics. A set of documentation for integration into device user's guides can be downloaded from <u>www.onarm.com</u> [1].

## ARM

+44 01223 400400, <u>www.arm.com</u> [2]

## Source URL (retrieved on 02/27/2015 - 6:05pm):

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## Links:

[1] http://www.onarm.com/[2] http://www.arm.com/