65 nm FPGA Family Adds Three Devices

Xilinx introduced three new devices and small form factor packaging to its 65nm Virtex-5 LX and LXT FPGA platforms. The addition of the Virtex-5 LX155 device to the logic-optimized LX platform, and the addition of the LX20T and LX155T devices plus a new small footprint 19 mm FF323 package to the Virtex-5 LXT platform with low power transceivers are intended to achieve cost-optimization for industrial networking, medical imaging, motor control, defense and high-performance computing applications. The Virtex-5 family offers designers the industry's most advanced, and available, 65nm FPGA technology and the only FPGAs with built-in PCI ExpressR Endpoint and tri-mode Ethernet MAC blocks. The Virtex-5 LXT platform gives designers an off-the-shelf serial connectivity solution that is said to save time, reduces power consumption and free up FPGA fabric resources. According to the company, the hardened PCI Express endpoint block saves users up to 10,000 LUTs and over 2W of power as compared to soft IP core implementations, while its 3.75 Gbps GTP transceivers consume <100 mW each at 3.2 Gbps. The company is also adding the Virtex-5 LX20T device to enable customers to implement serial standards into a low-density device. For applications such as motor control, it offers designers a single device solution for integrating microcontroller, waveform generation, networking protocols and other functions in an FPGA with low-logic consumption. The Virtex-5 LX20T is pin compatible with the Virtex-5 LX30T for design migration. The devices' package migration capability enables customers to design in either Virtex-5 LX110 and LX110T devices or LX220 and LX220T devices in a package that is pin compatible with the new LX155 and LX155T devices. The DSP optimized Virtex-5 SX95T device is also pin compatible with the Virtex-5 LX155 and LX155T devices.

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