

## FPGA-Based Deserializer

Xilinx, Inc. announces its Virtex-4 FPGA-based deserializer reference design, application note and evaluation module jointly developed with Texas Instruments. The reference design, along with the accompanying application note, deserializes data streams from TI's ADS6000 analog-to-digital converter (ADC) family, providing designers with an easy way to deploy products based on the latest advanced ADC device technology. The evaluation module, available through TI (TSW1200EVM), consists of a circuit board and a set of preconfigured design files which enable designers to prototype and evaluate the performance of the latest high-speed ADCs featuring serialized LVDS outputs. Applications include broadcast, medical, instrumentation and wireless infrastructures. The deserializer reference design accepts up to four simultaneous ADC channels and provides automatic de-skew and clock alignment functions.

Xilinx, Inc.

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