Analog and Serial I/O Modules for FPGA-based PMCs

VMETRO announced a trio of new I/O modules for use with its FPGA-based PMC products. Users can choose the front-end processing appropriate to the application from a dual channel 105 MSPS analog input, a dual channel 210 MSPS analog output or a RS-485 module. All three I/O modules integrate into a VMETRO PMC-FPGA03, PMC-FPGA05 or DEV-FPGA05 card. These FPGA-based PMC/PCI boards feature Xilin VirtexT-II Pro or Virtex-5 FPGAs and memory banks directly connected to the FPGA for signal and data processing in computationally intensive applications. Designed for DSP applications including Software Defined Radio (SDR), these products provide processing and I/O in a single, compact and flexible platform. The ADC-MOD1 module has a dual channel 105 MSPS 14-bit analog front-end. It includes an on-module 105 MHz sample clock and the ability to accept an external reference and trigger input. Four pairs of LVDS I/O are provided for general purpose use such as synchronizing multiple boards. Complementing the analog input module is a dual channel 210 MSPS 14-bit analog output module. The DAC-MOD1 module also has provision for an external clock source, trigger input and general LVDS I/O signals. The products also feature a 210 MHz on-module oscillator. The third module, the RS485-MOD2, is a 33 channel RS485/RS422 transceiver. For flexibility, all channels can be programmed to be inputs or outputs. Applications for this module include digital camera interfaces and general purpose control.

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281-584-0728, www.vmetro.com [1]

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