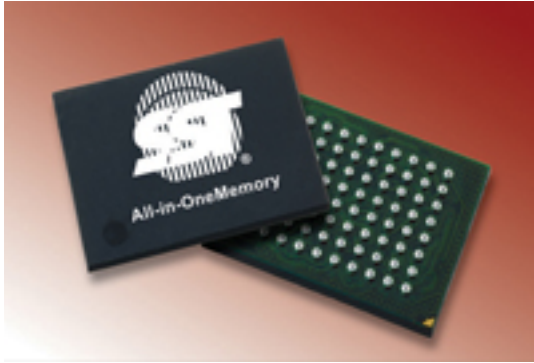


## All-in-one Memory Family for Embedded Applications



SST is sampling the first in its All-in-OneMemory product family, the SST88VP1107. By blending the features of NOR, NAND and RAM in a unified architecture, this product comes configured with 512 KByte instant-on boot NOR, 128 MB execute-in-place (XIP) code storage, 120 MB data storage and 12 MB system RAM for mobile and embedded applications. The single-bus, single-package product offers a completely managed memory subsystem in a 10 mm x 13 mm x 1.4mm LBGGA package. With code storage (NOR), data storage (NAND) and system RAM (PSRAM) functions all on a single PSRAM bus, the SST88VP1107 is suited for applications that need high-density memory and high reliability. Using the company's Pseudo-NOR (PNOR) technology, All-in-OneMemory optimizes the use of NAND Flash for both code and data storage. The PNOR area provides 128 MB XIP code storage and emulates high density NOR by using NAND as nonvolatile storage and PSRAM as cache. In the SST88VP1107, 4 MB of PSRAM is configured as cache for the PNOR area. Caching NAND flash content helps extend endurance and improve reliability of NAND flash by minimizing direct read/write access to the NAND flash. The inherent demand paging scheme of the cache also eliminates the need for shadowing XIP code, thus reducing the size of required built-in system RAM. The product offers a memory-mapped ATA (mATA) area providing 120 MB of data storage on the PSRAM bus, eliminating the need for an ATA physical interface. In addition, the SST88VP1107 offers a built-in Flash File System (FFS), Memory Management Unit (MMU) and hardware Error Checking and Correcting (ECC) engine.

Silicon Storage Technology  
408-735-9110, [www.sst.com](http://www.sst.com) [1]

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## **All-in-one Memory Family for Embedded Applications**

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### **Links:**

[1] <http://www.sst.com/>