

## **Call for Papers Deadline Extended to October 22 for CSTIC 2013; New Session Announced**

SEMI

**SHANGHAI, China — October 15, 2012** — SEMI announced today that abstracts for paper presentations are still being accepted for the CSTIC 2013 exhibition, which will be held March 17-18, 2013 in Shanghai, China, in conjunction with SEMICON China 2013. In addition, a new session, “Circuit Design, System Integration and Application” was announced. Last year, the highly successful annual event featured 100 technical lectures, 300 speakers, nearly 1,000 attendees and keynotes from Prof. Leo Esaki, Nobel Laureate and IBM Fellow; Dr. Kinam Kim, Samsung Fellow, president and CEO of Samsung Institute of Advanced Technology; and Dr. Luc Van den hove, president and CEO of IMEC, Belgium. CSTIC currently has just a few openings remaining for abstracts on semiconductor technology and manufacturing; the deadline is extended to October 22.

CSTIC is the largest annual semiconductor technology conference for the industry in China — providing an enabling platform for executives, managers, engineers and researchers to exchange the latest developments in semiconductor technology and manufacturing. It also offers opportunities for those who are interested in investing and collaboration in the semiconductor industry in Asia, particularly in China. The event is organized by SEMI and ECS and co-organized by China's High-Tech Expert Committee (CHTEC). It is co-sponsored by IEEE, MRS and China Electronics Materials Industry Association.

Confirmed CSTIC 2013 plenary speakers include: Prof. Ivar Giaever, Nobel Laureate and professor, RPI, USA; Dr. Ghavam Shahidi, IBM Fellow and director of Silicon Technology, IBM, USA, who is also the father of SOI technology. CSTIC features 10 symposia covering all aspects of semiconductor technology and manufacturing, including: Device Engineering and Technology; Lithography and Patterning; Dry & Wet Etch and Cleaning; Thin-Film Technology; CMP, Wafer Substrate Polishing and Post-Polish Cleaning; Materials and Process Integration for Device and Interconnection; Packaging and Assembly; Metrology, Reliability and Testing; Emerging Semiconductor Technologies; Advances in MEMS and Sensor Technologies; Circuit Design, System Integration and Applications.

Original and overview papers from integrated device manufacturers (IDMs), materials suppliers and equipment vendors as well as academic and research institutes are encouraged. Abstracts of approximately 200-500 words and a brief speaker biography are due October 22. The abstract should clearly detail the nature, scope, content, organization, key points and significance of the proposed paper and presentation. The abstract template is available on the conference

website.

For more conference information about CSTIC 2013, please visit <http://semiconchina.semi.org/cstic> or contact SEMI at [cstic@semi.org.cn](mailto:cstic@semi.org.cn), or call 86.21.50270909\*255. CSTIC 2013 also offers program sponsorship possibilities with benefits to enhance the presence of your company in China, one of the most dynamic markets in the world. These opportunities are available in several different forms; for details, please contact us at [cstic@semi.org.cn](mailto:cstic@semi.org.cn) [1].

## About SEMI

SEMI is the global industry association serving the nano- and microelectronics manufacturing supply chains. Our 2,000 member companies are the engine of the future, enabling smarter, faster and more economical products that improve our lives. Since 1970, SEMI has been committed to helping members grow more profitably, create new markets and meet common industry challenges. SEMI maintains offices in Beijing, Bengaluru, Berlin, Brussels, Grenoble, Hsinchu, Moscow, San Jose, Seoul, Shanghai, Singapore, Tokyo, and Washington, D.C. For more information, visit [www.semi.org](http://www.semi.org) [2]

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[1] <http://www.semi.org/en/node/mailto:cstic@semi.org.cn?subject=Call%20for%20Papers>

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