AltaSens Adopts Berkeley Design Automation AFS Platform for HD CMOS Image Sensors

Berkeley Design Automation, Inc. announced that AltaSens, Inc., a supplier of high-performance CMOS image sensors, has selected the company's AFS Platform for full-circuit post-layout verification, block-level characterization, and device noise analysis of their high-definition (HD) and wide-dynamic-range CMOS image sensors.

"At AltaSens we have significant circuit verification challenges to deliver CMOS image sensors with the highest image quality along with lower noise and lower power in a cost-effective solution," said Manjunath Bhat, VP of Engineering at AltaSens. "With the AFS Platform we can run post-layout verification of our high-performance wide-dynamic-range HD CMOS image sensors with nanometer SPICE accuracy. When running AFS single core we get performance on par with our existing parallel simulator running on 8 cores."

The Analog FastSPICE Platform is the world's fastest nanometer circuit verification platform for analog, RF, mixed-signal, and custom digital circuits. The AFS Platform delivers foundry-certified nanometer SPICE accuracy 5x-10x faster than any other simulator on a single core and an additional 2x-4x performance with multithreading. For circuit characterization, the AFS Platform includes the industry's only comprehensive silicon-accurate device noise analysis and delivers near-linear performance scaling with the number of cores. For large circuits, it delivers >10M-element capacity, the industry's fastest near-SPICE-accurate simulation, and mixed-analog-digital co-simulation with leading Verilog simulators. Available licenses include AFS circuit simulation, AFS Transient Noise Analysis, AFS RF Analysis, AFS Co-Simulation, and AFS Nano SPICE.

"We are delighted that AltaSens, a premier supplier of high-definition and wide-dynamic-range CMOS image sensors, has adopted the AFS Platform," said Ravi Subramanian, president and CEO of Berkeley Design Automation. "AltaSens' selection further validates that Berkeley Design Automation is an essential partner to leading edge companies designing high-performance CMOS image sensors across a wide variety of applications."

Source URL (retrieved on *08/02/2014 - 3:27am*):
http://www.ecnmag.com/news/2011/11/altasens-adopts-berkeley-design-automation-afs-platform-hd-cmos-image-sensors