

## **Upcoming Technical Course on "DSP for FPGAs" in October in Munich**

Starnberg, 13 September 2011 - On October 18 to 20, 2011, qaqadu event gmbh - experts for technical education - invite to the upcoming short course on "DSP for FPGAs". The intensive 3 day DSP for FPGAs course in Munich, Germany, will present and examine the use of FPGAs for DSP algorithms, applications and architectures. This course will feature the entire software design flow from concept, to bit true simulation, to actual hardware implementation on a Xilinx Virtex II Pro device. Special attention is paid to practical experience; after each presentation a hands-on session will follow in which attendees will be able to simulate and implement the structures and architectures introduced. Each attendee will receive a comprehensive set of notes and examples for in depth study after the course. This course will be led by Professor Bob Stewart and integrate presentations and design sessions from other experienced design engineers from SteepestAscent. It has been successfully presented in the USA at the University of California, Los Angeles (UCLA) twice per year since 2002, and each year at ISLI since 2004.

FPGAs with DSP functionalities are a rapidly growing market with great market potential. A broad field of applications reaching from industrial, automotive and even to communication systems like for example base stations, broadcast video SD/HD, image processing, infotainment, communications, radar, sonar and security systems. The growing requirements for processing speeds of the order of 10-100 billions of operations per second, the need for rapid prototyping and software definable architectures will further the penetration of FPGAs into the DSP communication market.

Further information and syllabus are available at <http://www.hightech-events.com/event/1/DSP-for-FPGAs> [1]

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**Links:**

[1] <http://www.hightech-events.com/event/1/DSP-for-FPGAs>