SPMT Consortium Releases New Specification

SUNNYVALE, Calif., October 1, 2009 — SPMT, LLC, the entity responsible for licensing the Serial Port Memory Technology (SPMT™) specification and administering the SPMT Consortium, today announced the release of the SPMT specification. The specification is available to SPMT members and can be accessed at http://www.spmt.org [1].

SPMT is the first-of-its-kind memory specification for dynamic random access memory (DRAM) based on a serial interface rather than the standard parallel interface. The technology initially targets the mobile handset market, enabling a new generation of devices capable of running highly data intensive, media-rich applications while at the same time extending battery life.

“The availability of the SPMT specification marks the beginning of a new era in general purpose memory technology for the mobile market,” said Jim Venable, president of SPMT, LLC. “The technology defined in the specification represents a huge leap forward in providing a low cost, high bandwidth and low power solution for the rapidly evolving mobile market enabling a whole new generation of products.”

The SPMT Consortium welcomes inquiries from companies and organizations interested in joining the group. Opportunities to participate in the advancement of Serial Port Memory Technology exist for contributors and members.

A webinar providing more information on the newly released specification will be held on October 22, 2009. To sign up for the webinar please visit http://www.spmt.org/resources/webinar.aspx [2].

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[1] http://www.spmt.org/

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