Texas Instruments DLP Announces Optimized Chipsets to Enable Brighter-Less Power Hungry Pico Projector Images

ECN Europe

Spain, 14th February – Driving forward the image quality of mobile pico projectors, Texas Instruments (TI) has revealed the optimized DLP Pico™ chipsets at Mobile World Congress. The chipsets are designed to enhance image brightness without increasing power usage for both WVGA (native DVD resolution) devices, such as mobile phones, and VGA devices, such as digital cameras and camcorders. TI DLP's Pico™ chip technology is already being adopted in the mobile sector via the first Android powered smartphone featuring a fully integrated DLP Pico projector. Able to project an image up to 50″ on any surface in optimum lighting conditions, the tiny projection chip requires very little space and is virtually undetected in the device's overall form factor. With advancements in size and performance, the TI DLP Pico chipset continues to enable the most compelling big picture experiences with contemporary handsets.

"Pico projectors have already started to have a positive impact on users of mobile products," said Frank Moizio, Manager of DLP Pico Projection. "With business and consumer mobile phone users increasingly accessing the Internet via smartphones and sharing growing amounts of multimedia content with colleagues and friends, pico projectors embedded into mobile devices are the perfect way to elevate those experiences. Integrated projectors using these new chipsets are the next step to making those experiences even more compelling, engaging, and hassle free, all from the palm of your hand."

TI's DLP Pico chipsets enable multimedia content to be displayed brighter than ever before, and with power-optimized lower voltage technology, battery consumption is kept to a minimum. Along with integration with the TI Analog ASIC – PAD 1000, these chipsets will enable a lower bill of material cost, smaller engine form factors and lower power requirements.

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